

2151

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re application of: William J. Armstrong et al. : Date: November 7, 2001
Group Art Unit: 2182 : IBM Corporation
Examiner: Unassigned : Intellectual Property Law
Serial No.: 09/838,057 : Dept. 917, Bldg. 006-1
Confirmation No.: 8659 : 3605 Highway 52 North
Filed: April 19, 2001 : Rochester, MN 55901

Title: METHOD AND APPARATUS FOR
ALLOCATING PROCESSOR
RESOURCES IN A LOGICALLY
PARTITIONED COMPUTER
SYSTEM

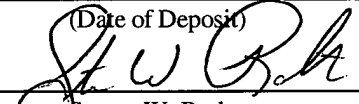
Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

I hereby certify that this correspondence is being
deposited with the United States Postal Service as
first class mail in an envelope addressed to:
Assistant Commissioner for Patents, Washington,
D.C. 20231, on

November 7, 2001

(Date of Deposit)


Steven W. Roth

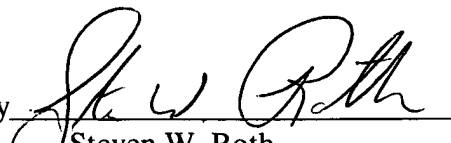
Letter to Official Draftsperson

Subject to the approval of the Examiner, please enter the drawings
(3 duplicate sets of sheets, each on A4 paper) included herewith in the above-identified
Application.

It is submitted that no new matter is added over the drawings originally provided.

The Commissioner is authorized to charge any comparison fees associated with
this submission to Deposit Account number 09-0465. A duplicate copy of this letter is
provided.

Respectfully submitted,

By 
Steven W. Roth
Registration No.: 34,712

Telephone: (507) 253-1600
Fax No.: (507) 253-2382

Docket No.: ROC920000252US1
Serial No.: 09/838,057

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Technology Center 2100